



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of: )  
Hisashi OHTANI et al. ) Art Unit: Unassigned  
Serial No. 09/837,552 ) Examiner: Unassigned  
Filed: 04/19/2001 )  
For: SEMICONDUCTOR DEVICE AND )  
MANUFACTURING METHOD THEREOF )

**INFORMATION DISCLOSURE STATEMENT AND**  
**NOTICE OF RELATED APPLICATIONS**

Commissioner for Patents  
Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the reference(s) listed on the attached Form PTO-1449 be made of record in the above-identified application. Copies of the references are submitted herewith in accordance with 37 C.F.R. 1.98(a).

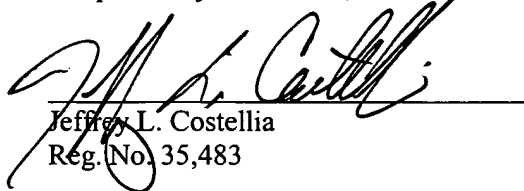
The Examiner's attention is directed to related U.S. patent applications:

Serial No.	Filing Date:
1) 09/837,877	April 19, 2001
2) 09/837,558	April 19, 2001

JP 2000-183356 is related to application Serial No. 09/837,552; JP 2000-194014 to application Serial No. 09/837,877; and JP 2000-196093 to application Serial No. 09/837,558. US Patent Nos. 5,917,221 and 5,807,772 are related to Application Serial No. 09/237,854.

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 19-2380. A duplicate copy of this sheet is attached.

Respectfully submitted,



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